



DESCRIPTION

The SSF8822 uses advanced trench technology to provide excellent R_{DS(ON)}, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a uni-directional or bi-directional load switch, facilitated by its common-drain configuration.

GENERAL FEATURES

• V_{DS} = 20V, I_D = 7A

- $R_{DS(ON)} < 21m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} < 24m\Omega @ V_{GS} = 4.5V$
- $R_{DS(ON)} < 28m\Omega @ V_{GS} = 3.6V$
- $\begin{array}{l} \mathsf{R}_{\mathsf{DS(ON)}} < 32 \mathrm{m}\Omega ~\textcircled{0} ~\mathsf{V_{GS}} = 2.5 \mathsf{V} \\ \mathsf{R}_{\mathsf{DS(ON)}} < 50 \mathrm{m}\Omega ~\textcircled{0} ~\mathsf{V_{GS}} = 1.8 \mathsf{V} \end{array}$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- Battery protection
- Load switch
- Power management

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
SSF8822	SSF8822	TSSOP-8	Ø330mm	12mm	3000 units

ABSOLUTE MAXIMUM RATINGS(TA=25 °C unless otherwise noted)

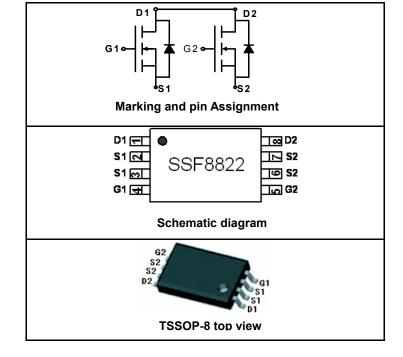
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	Vgs	±12	V
Drain Current Continuous@ Current Ruland (Note 1)	I _D	7	A
Drain Current-Continuous@ Current-Pulsed (Note 1)	I _{DM}	30	А
Maximum Power Dissipation	PD	1.5	W
Operating Junction and Storage Temperature Range	T_{J},T_{STG}	-55 To 150	°C

THERMAL CHARACTERISTICS

hermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	83	°C/W
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ELECTRICAL CHARACTERISTICS (TA=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V Ι _D =250μΑ	20			V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =16V, V_{GS} =0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±10V, V_{DS} =0V			±100	nA





ON CHARACTERISTICS (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =1mA	0.5	0.8	1	V	
		V _{GS} =10V, I _D =7A		16.4	21		
		V _{GS} =4.5V, I _D =6.6A		19	24		
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =3.6V, I _D =6A		21.7	28	mΩ	
		V _{GS} =2.5V, I _D =5.5A		25	32		
		V _{GS} =1.8V, I _D =2A		36	50		
Forward Transconductance	g fs	V _{DS} =5V,I _D =7A		24		S	
DYNAMIC CHARACTERISTICS (Note4)							
Input Capacitance	C _{lss}			630		PF	
Output Capacitance	C _{oss}	V _{DS} =10V,V _{GS} =0V, F=1.0MHz		160		PF	
Reverse Transfer Capacitance	C _{rss}			135		PF	
SWITCHING CHARACTERISTICS (Note 4)							
Turn-on Delay Time	t _{d(on)}			5.7		nS	
Turn-on Rise Time	tr	- V _{DS} =10V, R∟=1.4Ω		11.5		nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =5V, R_{GEN} =3 Ω		31.5		nS	
Turn-Off Fall Time	t _f			9.7		nS	
Total Gate Charge	Qg			9.3		nC	
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =7A, V _{GS} =4.5V		0.6		nC	
Gate-Drain Charge	Q _{gd}			3.6		nC	
Body Diode Reverse Recovery Time	trr	IF=7A, dl/dt=100A/µs		15.2		nS	
Body Diode Reverse Recovery Charge	Qrr	IF=7A, dl/dt=100A/µs		6.3		nC	
DRAIN-SOURCE DIODE CHARACTERISTIC	S						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1A		0.7	1	V	
Diode Forward Current (Note 2)	I _S				2.5	А	

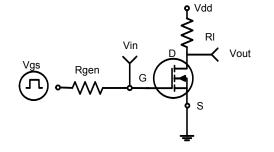
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NOTES:

Repetitive Rating: Pulse width limited by maximum junction temperature.
 Surface Mounted on FR4 Board, t ≤ 10 sec.
 Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
 Guaranteed by design, not subject to production testing.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



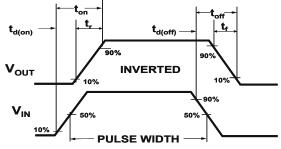


Figure 1:Switching Test Circuit

Figure 2:Switching Waveforms

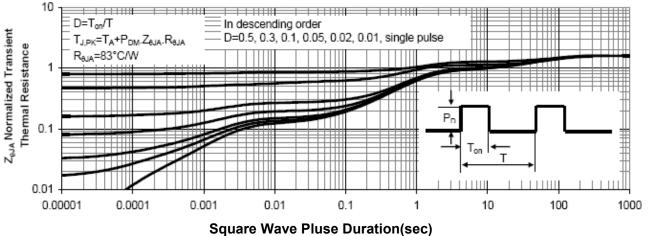
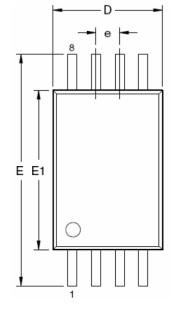


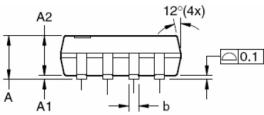
Figure 3: Normalized Maximum Transient Thermal Impedance

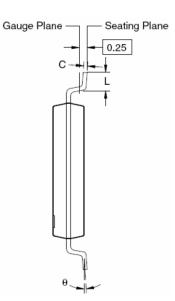
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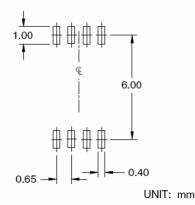
TSSOP-8 PACKAGE INFORMATION







RECOMMENDED LAND PATTERN



Dimensions in millimeters

Symbols	Min.	Nom.	Max.	
А	_	_	1.20	
A1	0.05	—	0.15	
A2	0.80	1.00	1.05	
b	0.19	—	0.30	
С	0.09	—	0.20	
D	2.90	3.00	3.10	
E	6.40 BSC			
E1	4.30	4.40	4.50	
е	0.65 BSC			
L	0.45	0.60	0.75	
θ	0 °	—	8°	

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Dimensions in inches

Symbols	Min.	Nom.	Max.
A	_	_	0.047
A1	0.002	—	0.006
A2	0.031	0.039	0.041
b	0.007	_	0.012
С	0.004	_	0.008
D	0.114	0.118	0.122
E	0.252 BSC		
E1	0.169	0.173	0.177
е	0	.026 BSC	
L	0.018	0.024	0.030
θ	0 °	—	8°

NOTES:

- 1. Dimensions are inclusive of plating
- Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
 Dimension L is measured in gauge plane.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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